Certificate / Certificat

Zertifikat / 合格証

Micron 21/03-104 R019

exida hereby confirms that the:

Functional Safety Management Process for SDRAM IC Hardware Development

Micron Technology, Inc.
Boise (ID), USA

Has been assessed per the relevant requirements of:

ISO 26262 : 2018 Parts 2, 4, 5, 7, 8 and 9

and meets the requirements providing a level of integrity to:

Systematic Capability: ASIL D Capable

The manufacturer may use the mark:

Revision 1.0 June 4, 2022
Surveillance Audit Due
July 1, 2025

Functional Safety Management (FSM) process

The Micron FSM process for SDRAM IC Hardware Development comprises of process descriptions, procedures, guidelines, tools and templates, that are used within Micron for the development of SDRAM memory ICs targeting automotive safety applications.

Application restrictions

This FSM certification applies to Micron Technology, Inc. in Boise (USA) and its affiliated companies and sites worldwide.

This FSM certification does not make any statement about the conformity to the ISO 26262 standard of any Micron product. The correct application of this FSM process during a product development project and the achievement of functional safety on product level shall be subject to a separate ISO 26262 functional safety assessment.

Evaluating Assessor

Certifying Assessor

ANSI National Accreditation Board
ACCREDITED
ISO/IEC 17065
PRODUCT CERTIFICATION BODY
#1004

Certificate / Certificat / Zertifikat / 合格証

Functional Safety Management Process for SDRAM IC Hardware Development

Systematic Capability: ASIL D Capable

FSM Process for SDRAM IC Hardware Development

Process Overview

The Micron FSM process is a functional safety management and IC hardware development process used by Micron for the development of SDRAM memory IC products. The ISO 26262 functional safety standard and safety lifecycle have been tailored to the specific characteristics and requirements of Micron SDRAM memory products.

ISO 26262 Functional Safety Standard	Tailoring
Part 2: Management of functional safety	Fully applicable
Part 3: Concept phase	Not applicable
Part 4: Product development at the system level	Partially
Part 5: Product development at the hardware level	Fully applicable
Part 6: Product development at the software level	Not applicable
Part 7: Production, operation, service and decommissioning	Partially
Part 8: Supporting processes	Partially
Part 9: ASIL-oriented and safety-oriented analyses	Fully applicable

Systematic Capability: ASIL D Capable

The Micron FSM Process for SDRAM IC Hardware Development, as tailored and implemented by Micron, complies with the applicable functional safety management requirements of ISO 26262 for ASIL D.

The following documents are a mandatory part of this certification:

Assessment Report: Micron 21/03-104 R017, V1 R0

80 N Main St
Sellersville, PA 18960

T-048, V4R2